

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

SANDEEP KHANNA

Serial No. 10/644,454

Filed: August 19, 2003

For: PROGRAMMABLE DELAY
CIRCUIT WITHIN A CONTENT
ADDRESSABLE MEMORY

a Divisional of:

Serial No. 10/040,714

Filed: December 28, 2001

EXAMINER: PHAM, LY D.

ART UNIT: 2818

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Restriction Requirement mailed on March 26, 2004, applicant hereby elects the claims designated below for examination purposes.

FIRST CLASS CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450.

on 4/6/04
Date of Deposit

JUANITA BRISCOE
Name of Person Mailing Correspondence
Juanita Briscoe
Signature

4/6/04
Date